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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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(TBMB-0036)

Applicant: Gray et al.

Title: CIRCUIT AREA MINIMIZATION USING SCALING

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**STATUS INQUIRY**

Sir:

Applicant's representative respectfully requests the status of the above-referenced patent application.

Respectfully submitted,

Spencer K. Warnick, Reg. No. 40398  
Hoffman, Warnick & D'Alessandro LLC  
75 State Street, 14th Floor  
Albany, NY 12207  
Telephone: (518) 449-0044  
Fax: (518) 449-0047

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